



Docket No.: M4065.0381/P381
(PATENT)

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of:
Leonard Forbes, et al.

Application No.: 09/808,114

Group Art Unit: 2826

Filed: March 15, 2001

Examiner: Kevin V. Quinto

For: TECHNIQUE TO MITIGATE SHORT CHANNEL
EFFECTS WITH VERTICAL GATE TRANSISTOR
WITH DIFFERENT GATE MATERIALS

8/Electra
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/S

RESPONSE TO RESTRICTION REQUIREMENT

Commissioner for Patents
Washington, DC 20231

Dear Sir:

In response to the restriction requirement set forth in the Office Action mailed August 26, 2002 (Paper No. 7), applicant hereby provisionally elects Group I, claims 1-67, for continued examination, without traverse. Applicants believe the claims are in immediate condition for allowance and respectfully request that the Examiner forward a notice of allowance and subsequently pass the application to issue.

Dated: September 18, 2002

Respectfully submitted,

By

Thomas J. D'Amico

Registration No.: 28,371

Ryan H. Flax

Registration No.: 48,141

DICKSTEIN SHAPIRO MORIN &
OSHINSKY LLP

2101 L Street NW

Washington, DC 20037-1526

(202) 785-9700

Attorneys for Applicants

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